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BUS FRAMER

ABSTRACT

A bus framer includes an engine which extracts information from a frame of data being transmitted over a time-division multiplexed bus and a processor which retrieves the information from the engine over an internal bus and forwards the information. The bus framer further includes a mapper which maps the frame of data on the time-division multiplexed bus to a read/write bus and a functional module which receives the data from the read/write bus.

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